

# Модель прерываний в IA-64

## IA-64 Interruption Process

### Application Code

$IP \rightarrow$	0x1000	INST A
	0x1010	INST B
	0x1020	INST C
	:	:

**IVT C** RETURN TO APP CODE

$IP \rightarrow$	0x4000	
	0x4010	INST
	0x4020	RFI
	:	:



**BANK SWITCHING**

**Restoring Pre-Interruption State**

**1** Processor switches back to Bank 1 registers



### Current Processor State

$IP$	0x4020
$PSR$	
.	
.	

Processor restores state

### Interruption Registers

$IIP$	0x1010
$IPSR$	
.	
.	

**2** Processor restores state from interruption registers before returning from interrupt

